



501.43253X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Y. TAKAHASHI, et al  
Serial No.: 10/722,461  
Filed: November 28, 2003  
For: SEMICONDUCTOR MEMORY DEVICE AND  
SEMICONDUCTOR INTEGRATED CIRCUIT  
Group:

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 5, 2004

Sir:

The following preliminary amendments and remarks are respectfully  
submitted in connection with the above-identified application, as listed below and as  
set forth on the following pages:

Substitute Specification and Marked Up Version of Original Specification;  
Amendment of the Abstract;  
Amendment of the Claims; and  
Remarks are included following the amendments.